

CPLD update manual

What is JTAG and what is the difference in update between ARM and a CPLD:

On the board of the SF3 there are 2 large ICs, one of which is an ARM microprocessor, the other is a CPLD logic chip.

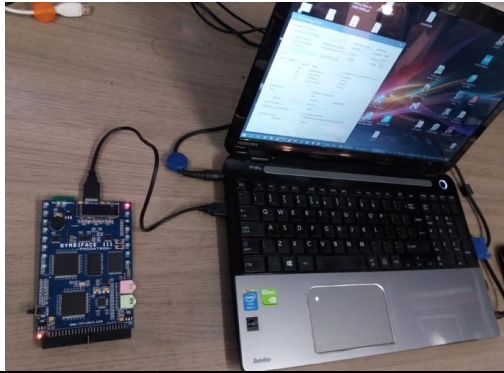


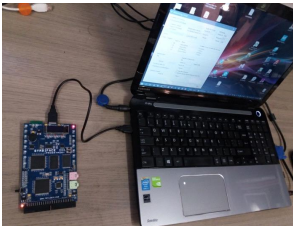
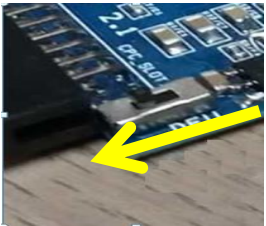
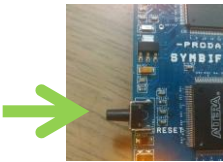
Normally with a DFU you reprogram the ARM, the ARM controls all processes of the SF3.

A CPLD makes a hardware connection between the Z80 and the ARM and MEMORY.


The SF3 has an onboard JTAG interface.

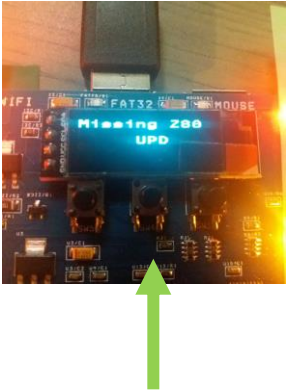
The ARM programs the CPLD via this JTAG interface


The code for the CPLD has been added in the DFU


<p><i>Normal DFU update:</i></p>		
		
<p><i>CPLD update:</i></p> <p>Leave the SF3 connected to your computer.</p> <p>press the DFU switch down (normal position)</p>		
<p>Reset the SF3</p>		

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<p>Oled shows: Missing Z80</p> <p>this is displayed when the CPC is NOT connected</p>	
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<p>Press the middle button (UPD)</p>	
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<p>the display shows: UPDATE</p> <p>the CPLD is now being updated</p>	
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<p><i>when the update is complete,</i></p> <p><i>the display shows: READY</i></p>	
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<p><i>The SF3 is ready to use</i></p>	
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<p><i>When it's go wrong:</i></p>	
<p>Reset the SF3:</p>	
<p>Try again</p>	
<p>If it still doesn't work after that. do not use the SF3, and contact TMTLOGIC. tmtlogic@gmail.com</p>	